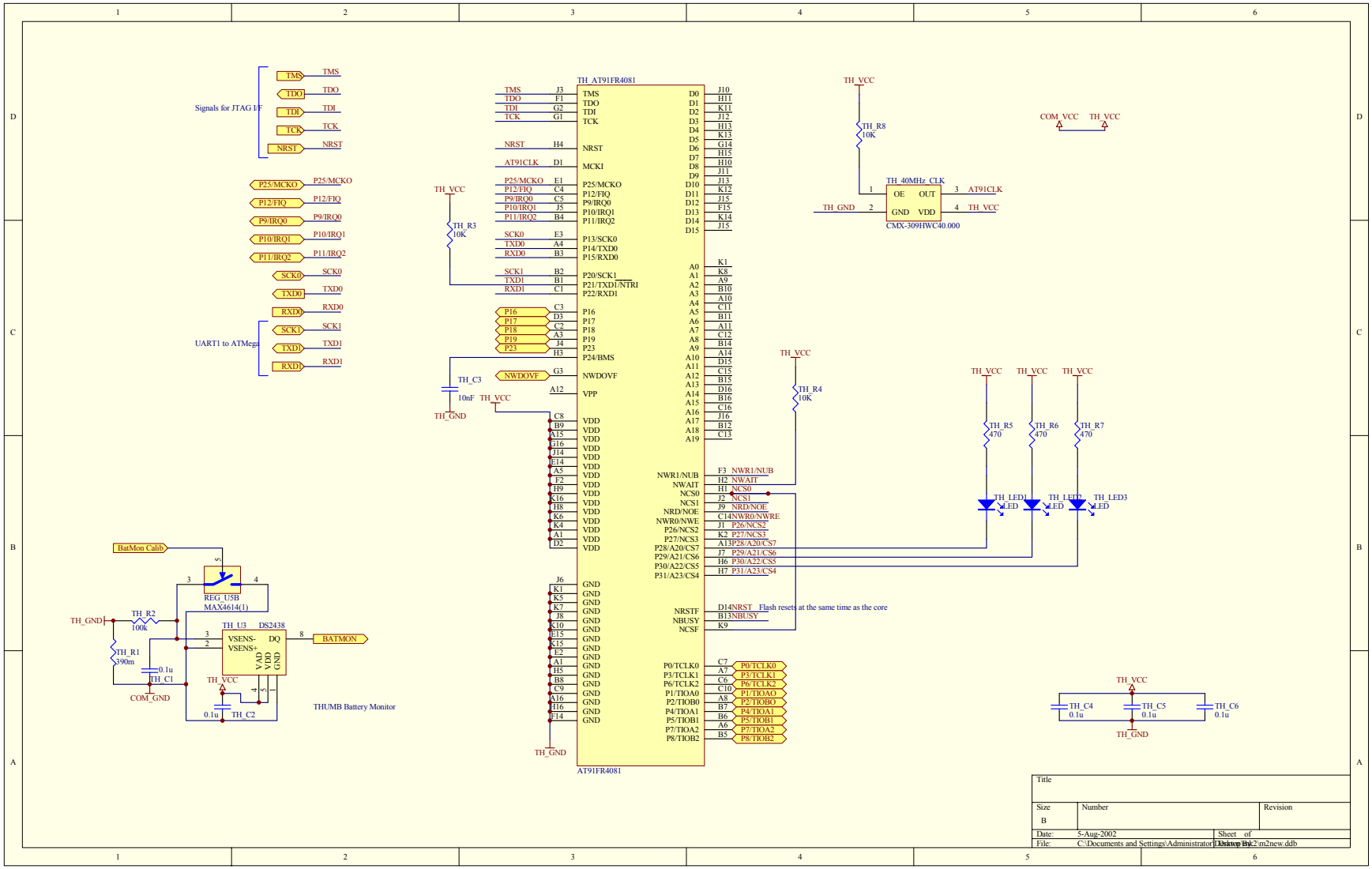
Signals for JTAG I/F
TMS
TDO
TDI
TCK
NRST
UART1 to ATMega
P25/MCKO
P12/FIQ
P9/IRQ0
P10/IRQ1
P11/IRQ2
SCK0
TXD0
RXD0
SCK1
TXD1
RXD1
TH_AT91FR4081
AT91FR4081
TH_VCC
TH_GND
TH_R3
10K
TH_C3
10nF
TH_R4
10K
TH_R8
10K
TH_40MHz_CLK
CMX-309HWC40.000
COM_VCC
TH_R5
TH_R6
TH_R7
470
TH_LED1
TH_LED2
TH_LED3
LED
NWR1/NUB
NWAIT
NCS0
NCS1
NRD/NOE
NWR0/NWE
P26/NCS2
P27/NCS3
P28/A20/CS7
P29/A21/CS6
P30/A22/CS5
P31/A23/CS4
NRSTF
NBUSY
NCSF
Flash resets at the same time as the core
P0/TCLK0
P3/TCLK1
P6/TCLK2
P1/TIOA0
P2/TIOB0
P4/TIOA1
P5/TIOB1
P7/TIOA2
P8/TIOB2
BatMon Calib
REG_USB
MAX4614(1)
TH_R2
100k
TH_R1
390m
TH_C1
0.1u
TH_U3
DS2438
VSENS-
VSENS+
DQ
VAD
VDD
GND
BATMON
TH_C2
0.1u
THUMB Battery Monitor
TH_C4
TH_C5
TH_C6
Title
Size
B
Number
Revision
Date:
5-Aug-2002
Sheet
of
File:
C:\Documents and Settings\Administrator\Desktop\m2new.ddb